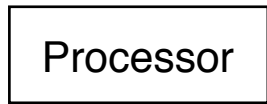
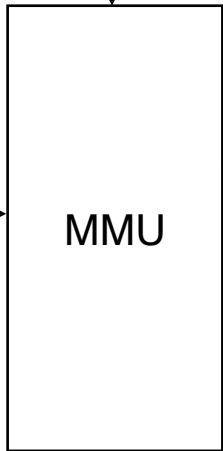


CPU chip

PTE

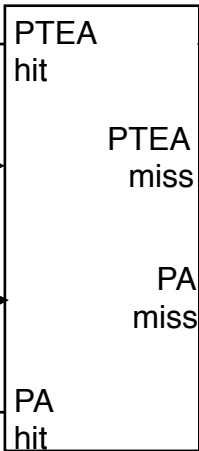


VA



PTEA

PA



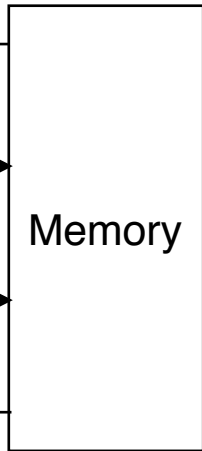
L1  
cache

PTE

PTEA

PA

Data



Data

