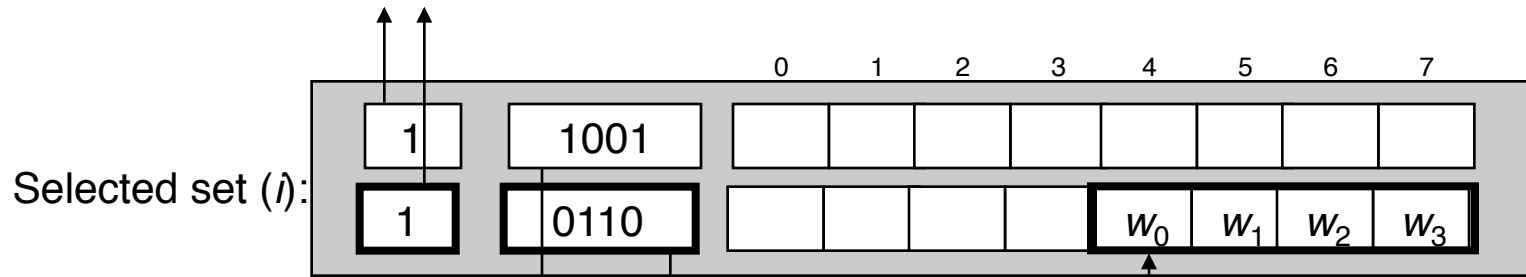
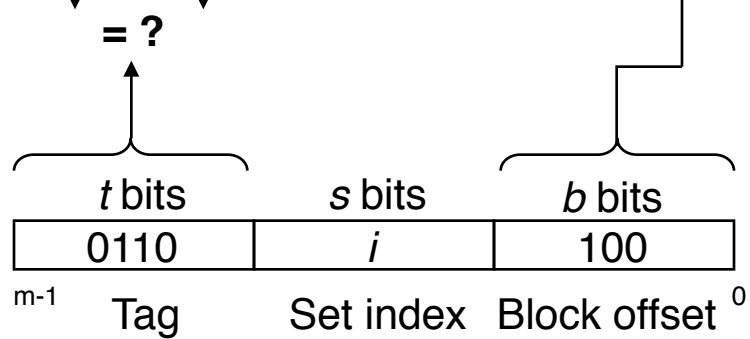


(1) The valid bit must be set



(2) The tag bits in one of the cache lines must match the tag bits in the address



(3) If (1) and (2), then cache hit, and block offset selects starting byte