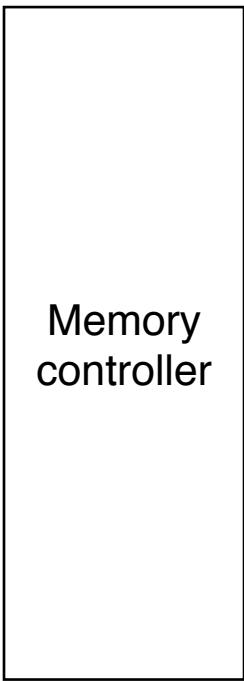


DRAM chip



RAS = 2

2

addr

8

data

Rows

Cols

0

1

2

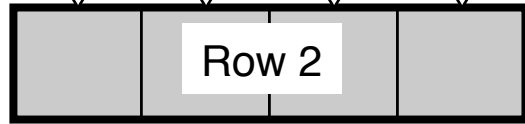
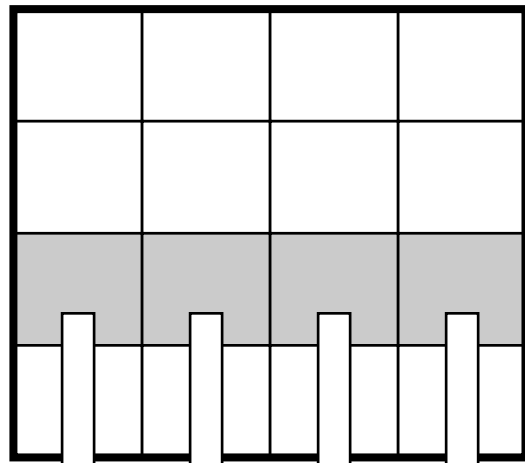
3

0

1

2

3



Internal row buffer