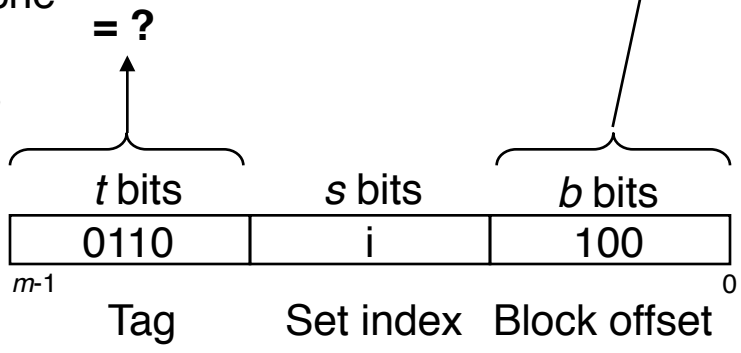


(2) The tag bits in the cache line must match the tag bits in the address



(3) If (1) and (2), then cache hit, and block offset selects starting byte.